



Packet No.: GR 00 P 1679

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Dieter Kantz et al.  
Applic. No. : 09/826,594  
Filed : April 5, 2001  
Title : Test Configuration for the Functional Testing of a  
Semiconductor Chip  
Art Unit : 2858

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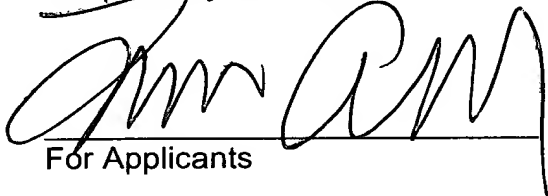
Hon. Commissioner of Patents and Trademarks,  
Washington, D.C. 20231

Sir:

Please recognize MARK P. WEICHSELBAUM (Reg. No. 43,248) as my associate in the matter in the above-identified application, with full powers. Please continue addressing all communications to the following address:

Lerner and Greenberg, P.A.  
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Respectfully submitted,



For Applicants

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Date: September 12, 2001

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